FIG. 1

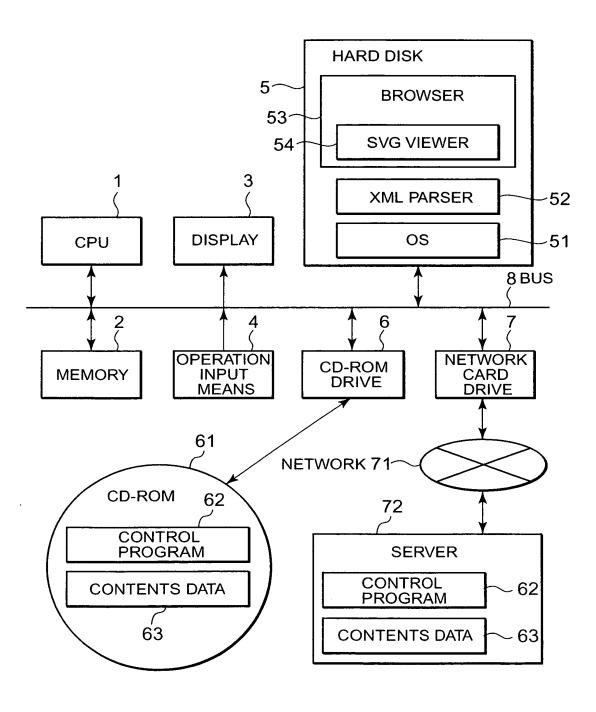


FIG. 2

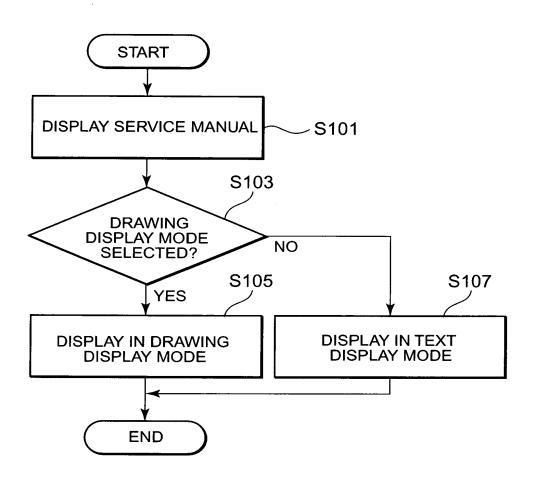


FIG. 3

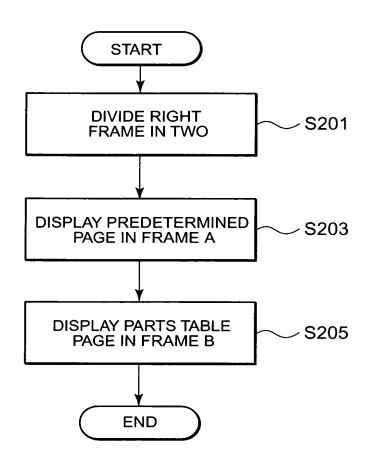
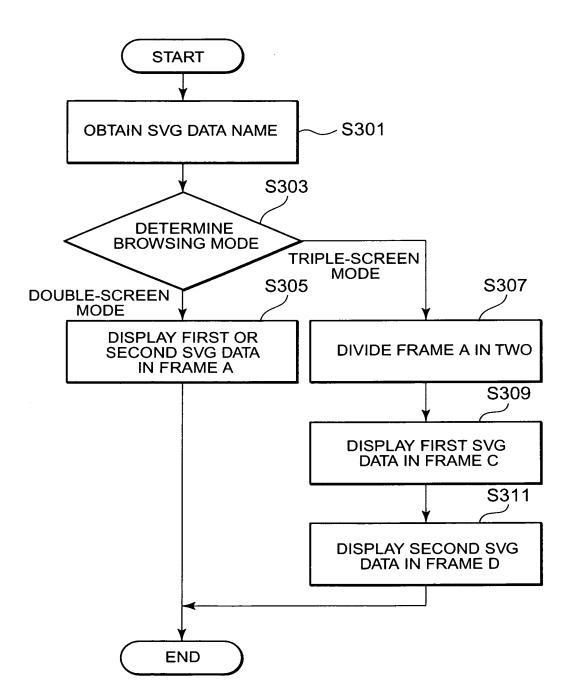


FIG. 4



2 P

FIG. 5

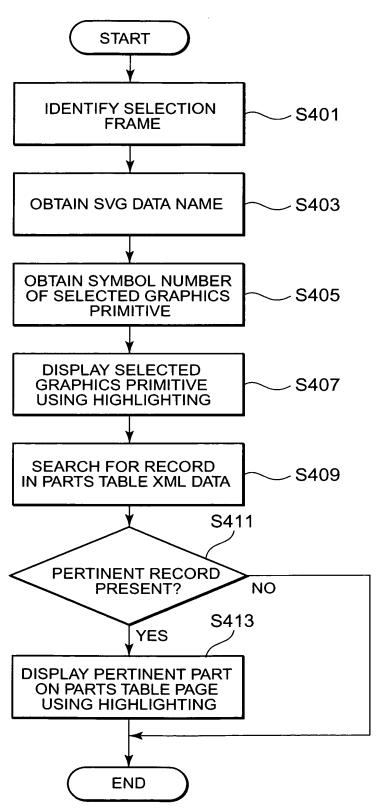


FIG. 6

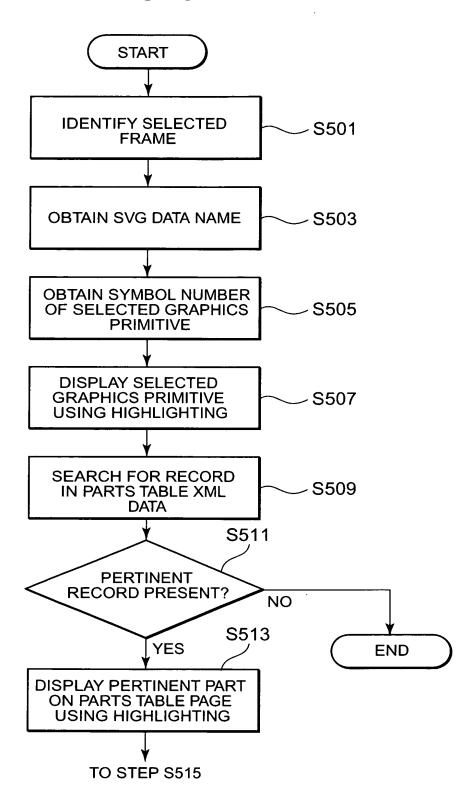


FIG. 7

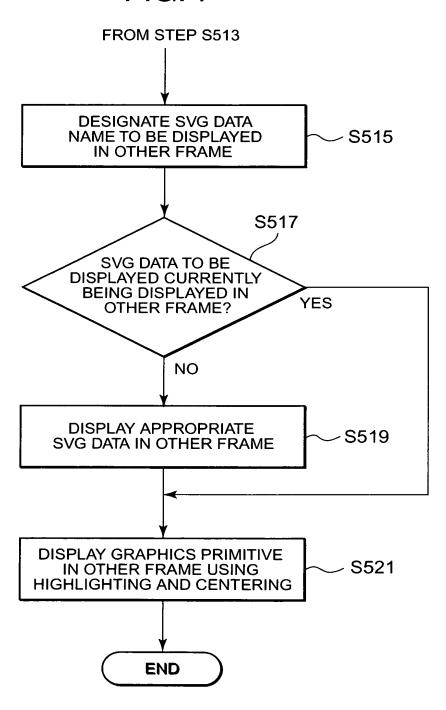
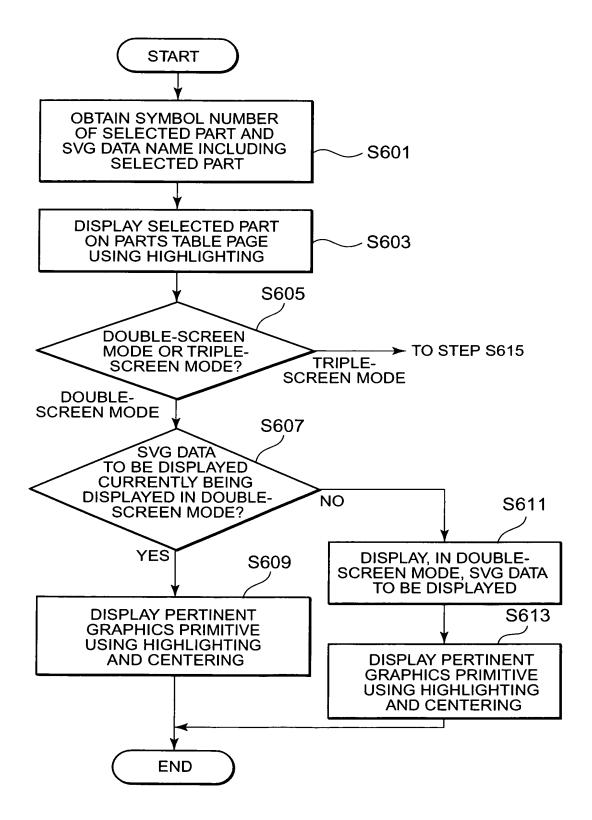


FIG. 8



7 1 . 1 . 1 . 1 . 1

FIG. 9

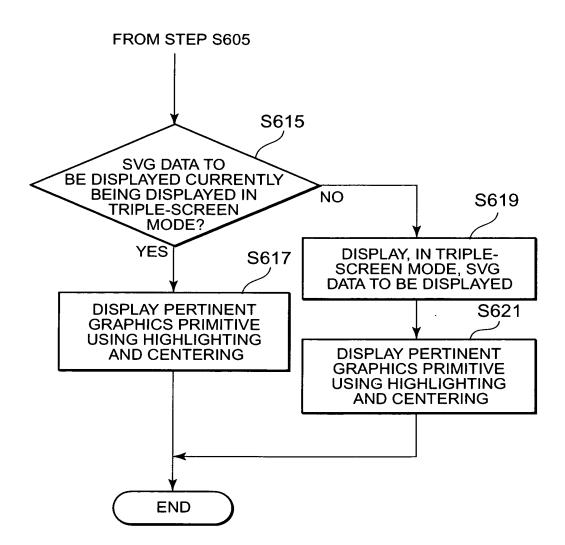
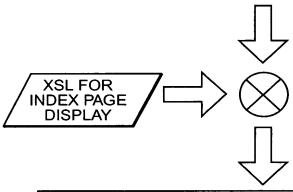


FIG. 10

INDEX XML DATA

CIRCUIT	BROWSING MODE	FIRST SVG DATA NAME	SECOND SVG DATA NAME
SERVO CIRCUIT	1	A1234_s001	A1234_p001
AV DECODER CIRCUIT	1	A1234_s002	A1234_p001
FLASH ROM CIRCUIT	1	A1234_s003	A1234_p002
VIDEO OUTPUT CIRCUIT	1	A1234_s004	A1234_p003
DIGITAL OUTPUT CIRCUIT	3	-	A1234_p003
SYSTEM CONFIGURATION CIRCUIT	0	A1234_s006	-
\$	\$	\$	\$



--> ≈ P 5

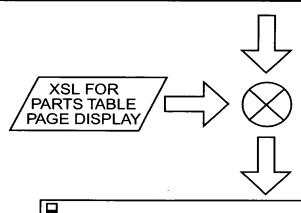
INDEX

- ▲ SERVO CIRCUIT
- ▲ AV DECODER CIRCUIT
- ▲ FLASH ROM CIRCUIT
- **▲ VIDEO OUTPUT CIRCUIT**
- ▲ DIGITAL OUTPUT CIRCUIT
- ▲ SYSTEM CONFIGURATION OUTPUT CIRCUIT

FIG. 11

PARTS TABLE XML DATA

RECORD	SYMBOL NUMBER		PART NAME	~	FIRST SVG DATA NAME	SECOND SVG DATA NAME
1	IC101	AN8703FH-V	IC	~	A1234_s001	A1234_p001
2	IC201	BA5983FM-X	IC	~	A1234_s001	A1234_p001
3	IC251	BA6664FM-X	IC	~	A1234_s001	A1234_p001
4	IC301	MN103S26EGA	IC	~	A1234_s002	A1234_p001
5	IC401	MN102L62GLF3	IC	~	A1234_s002	A1234_p001
6	IC451	S-93C66AFJ-X	IC	~	A1234_s003	A1234_p002
7	IC501	NDV8611VWA	IC	~	A1234_s003	A1234_p002
\$	\$	\$	\$	\$	\$	\$



	\Rightarrow	x P D		
PAR	TS TAI	BLE		
SYM	BOL //BER	PART NUMBER	PART NAME	~
IC	101	AN8703FH-V	IC	~
IC	201	BA5983FM-X	IC	~
IC	251	BA6664FM-X	IC	~
IC:	301	MN103S26EGA	IC	~
IC4	401	MN102L62GLF3	IC	~
IC4	451	S-93C66AFJ-X	IC	~
IC:	501	NDV8611VWA	IC	~
	5	\$	5	\$

î ·

FIG. 12

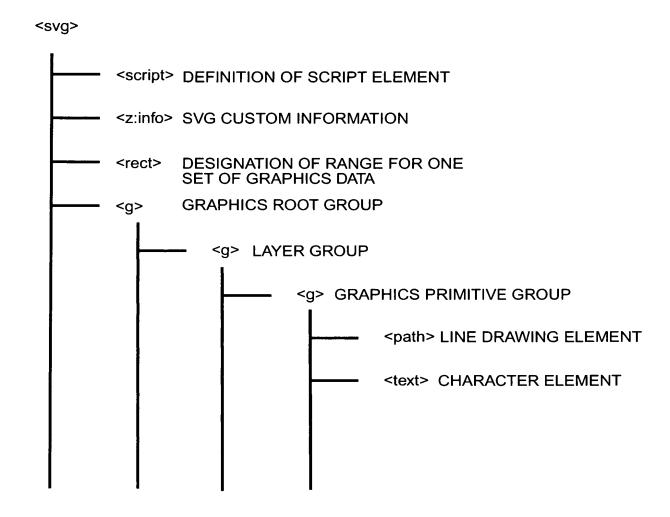


FIG. 13

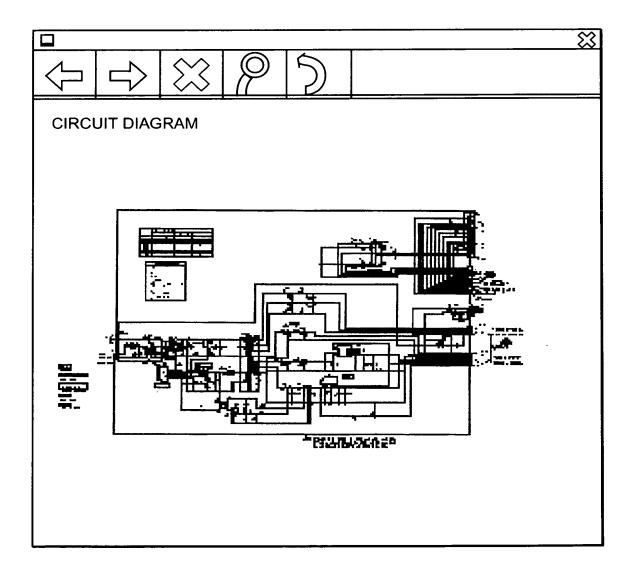
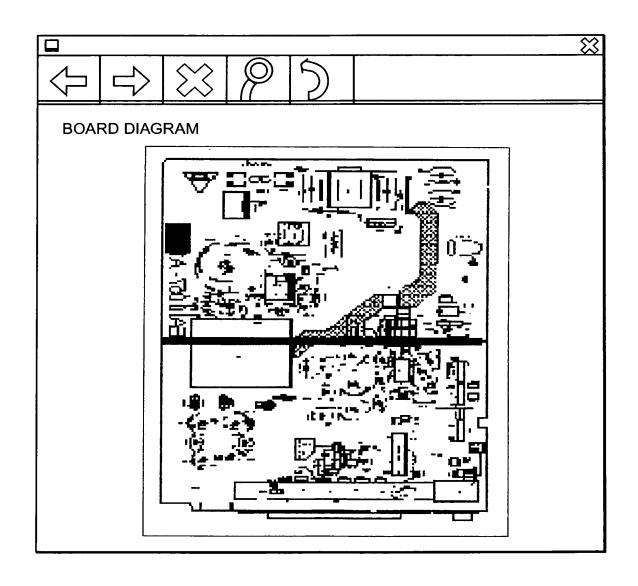


FIG. 14



15/23

-0.19008.40

FIG. 15

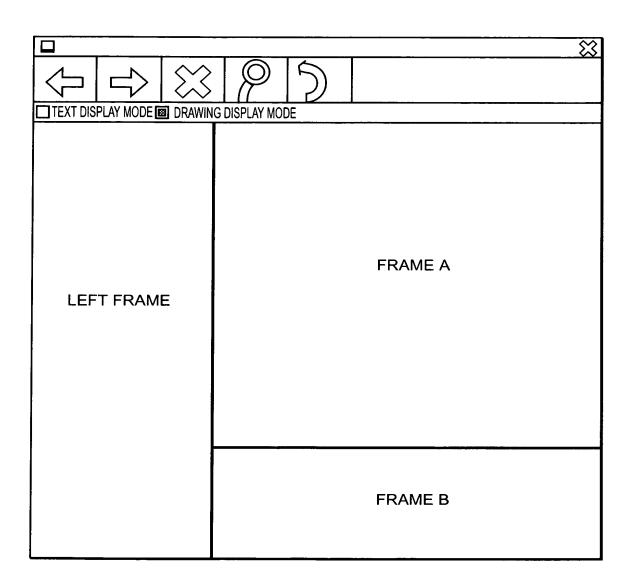


FIG. 16

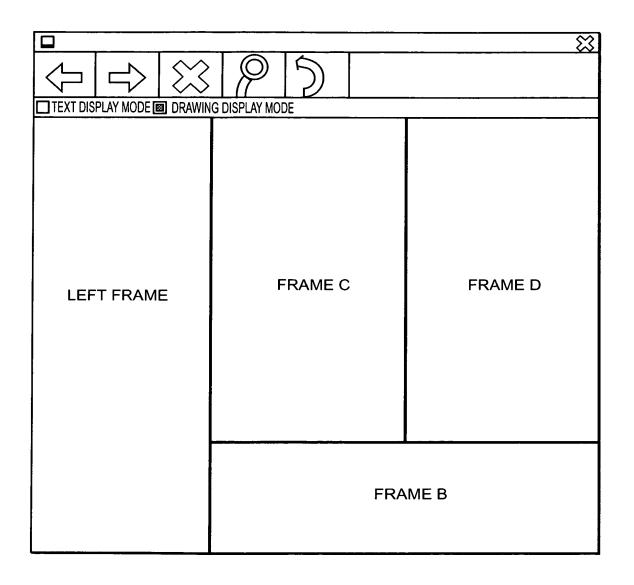


FIG. 17

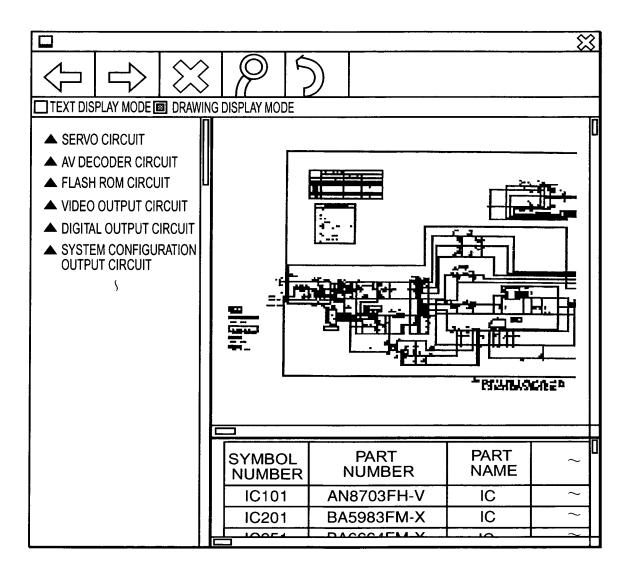
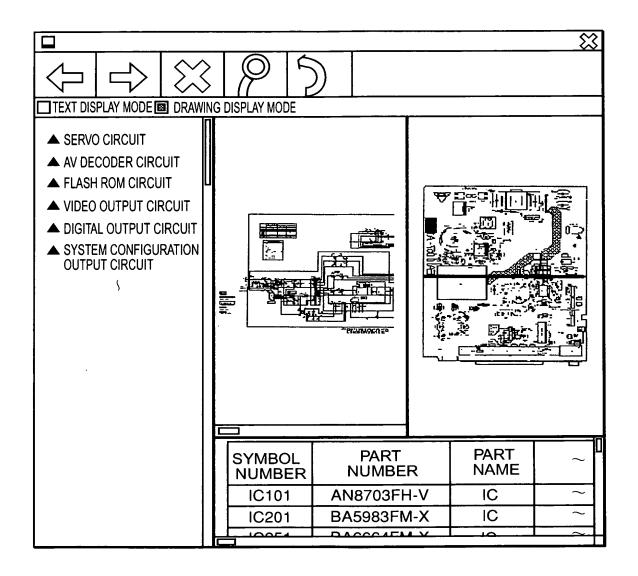


FIG. 18



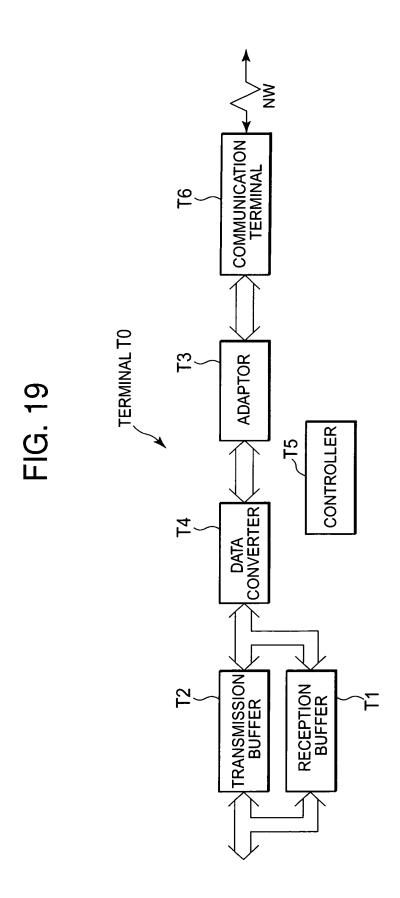
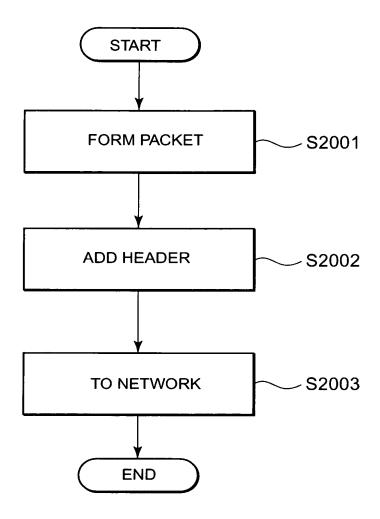


FIG. 20



21/23

FIG. 21

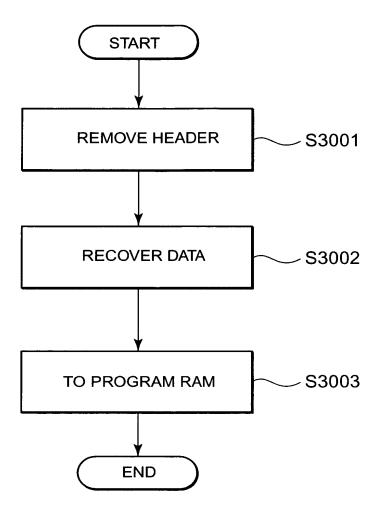
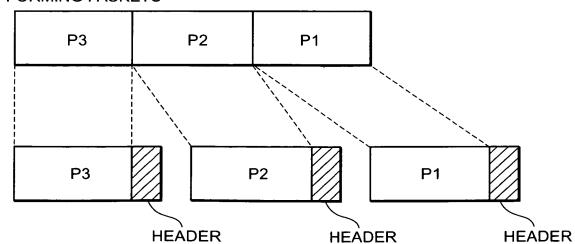


FIG. 22

FORMING PACKETS



67500250

FIG. 23

RECOVERING DATA

